



## UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE  
 United States Patent and Trademark Office  
 Address: COMMISSIONER FOR PATENTS  
 P.O. Box 1450  
 Alexandria, Virginia 22313-1450  
 www.uspto.gov

**\*BIBDATASHEET\*****CONFIRMATION NO. 5053**

Bib Data Sheet

SERIAL NUMBER 10/690,496	FILING DATE 10/21/2003  RULE	CLASS 324	GROUP ART UNIT 2829	ATTORNEY DOCKET NO. 2269-3038.3US (96-0936.3/
-----------------------------	---------------------------------------	--------------	------------------------	---

## APPLICANTS

Warren M. Farnworth, Nampa, ID;

William K. Waller, Garland, TX;

Leland R. Nevill, Boise, ID; Raymond J. Beffa, Boise, ID;

Eugene H. Cloud, Boise, ID;

**\*\* CONTINUING DATA \*\*\*\*\***

This application is a CON of 10/218,279 08/13/2002 PAT 6,636,068  
 which is a CON of 09/944,509 08/30/2001 PAT 6,452,415  
 which is a CON of 09/083,819 05/22/1998 PAT 6,313,658

Yes MT

**\*\* FOREIGN APPLICATIONS \*\*\*\*\***

None MT

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

**\*\* 01/20/2004**

Foreign Priority claimed 35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no Verified and Acknowledged <u>Muh Tong</u> Examiner's Signature <u>MS</u> Initials	STATE OR COUNTRY ID	SHEETS DRAWING 6	TOTAL CLAIMS 13	INDEPENDENT CLAIMS 4
--	---------------------------	------------------------	-----------------------	----------------------------

## ADDRESS

24247

TRASK BRITT

P.O. BOX 2550

SALT LAKE CITY , UT

84110

## TITLE

Device and method for isolating a short-circuited integrated circuit (IC) from other ICs on a semiconductor wafer

FILING FEE

FEES: Authority has been given in Paper

No. \_\_\_\_\_ to charge/credit DEPOSIT ACCOUNT

RECEIVED

No. \_\_\_\_\_ for following:

986

☐ All Fees☐ 1.16 Fees ( Filing )☐ 1.17 Fees ( Processing Ext. of time )☐ 1.18 Fees ( Issue )☐ Other \_\_\_\_\_☐ Credit